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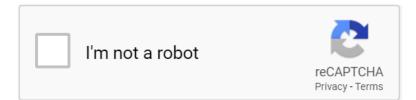


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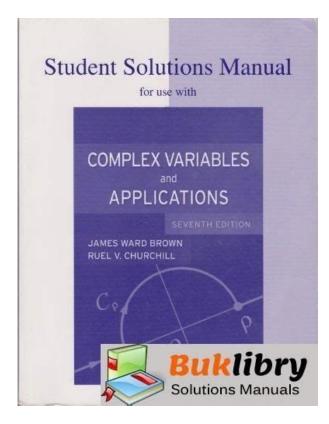
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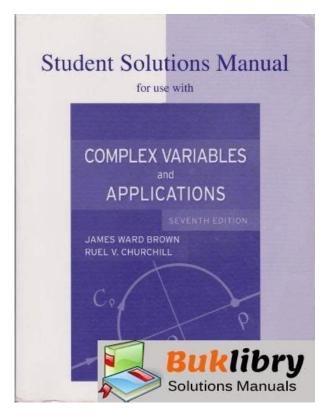


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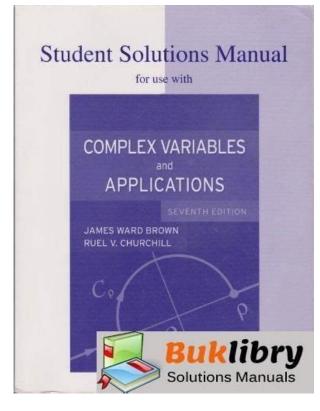


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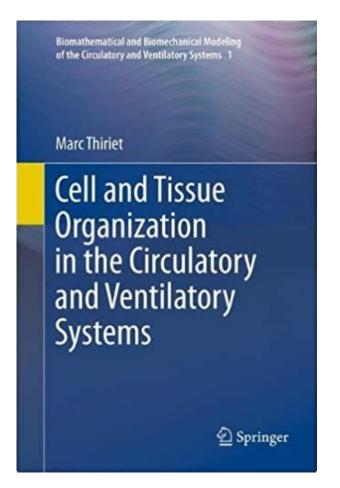


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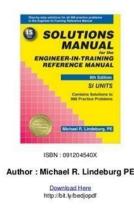
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New this Edition Each chapter includes two new, realworld examples, one mobile and one data center, to illustrate the revolutionary change to personal mobile devices and cloud computing. Expanded and improved coverage of multicore and GPU architectures. Completely new chapters on warehousescale cloud computers Chapter 6 and vector processors and GPUs Chapter 4. Improvements and updates throughout, including updated performance analysis data featuring the new SPECPower benchmark. There is a lot of content on processors for mobile computing, and power usage is a pervasive theme. At the other extreme there is an excellent chapter on warehouse scale computers, which offers tremendous insight into the cloud computing infrastructure provided by Google, Amazon and others. If your job has anything to do with IT infrastructure then I recommend this book as a mustread. As an academic text book it has both depth and breadth.Among his many awards are the 2001 EckertMauchly Award for his contributions to RISC technology, the 2001 Seymour Cray Computer Engineering Award, and the 2000 John von Neumann Award, which he shared with David Patterson. He has also received seven honorary doctorates. ACM named David A. Patterson a recipient of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry. David A. Patterson is the Pardee Chair of Computer Science, Emeritus at the University of California Berkeley. His teaching has been honored by the Distinguished Teaching Award from the University of California, the Karlstrom Award from ACM, and the Mulligan Education Medal and Undergraduate Teaching Award from IEEE. Patterson received the IEEE Technical Achievement Award and the ACM EckertMauchly Award for contributions to RISC, and he shared the IEEE Johnson Information Storage Award for contributions to RAID.

Like his coauthor, Patterson is a Fellow of the American Academy of Arts and Sciences, the Computer History Museum, ACM, and IEEE, and he was elected to the National Academy of Engineering, the National Academy of Sciences, and the Silicon Valley Engineering Hall of Fame. He served on the Information Technology Advisory Committee to the U.S. President, as chair of the CS division in the Berkeley EECS department, as chair of the Computing Research Association, and as President of ACM. This record led to Distinguished Service Awards from ACM, CRA, and SIGARCH.To calculate the overall star rating and percentage breakdown by star, we don't use a simple average. Instead, our system considers things like how recent a review is and if the reviewer bought the item on Amazon. It also analyzes reviews to verify trustworthiness. Please try again later. Henry 4.0 out of 5 stars I wish it was structured a little better. It jumps around quite a bit. Its not an easy read for a person like me with a mechanical engineering background. But as of now, its a good reference book. I would recommend it. The diagrams help. But the contents do seem a bit outdated and Im sure there may be books out there with better layouts. Thank you Prof. Hennessy and Patterson, as well as all other contributors for writing such an approachable book, not only for students, but also for practitioners. This edition brings the book up to date with the developments in computer architecture and various surrounding technologies, such as memory, disk, etc. The GPU chapter was fun to read. It explains numerous avenues to pursue performance gains, and gives actual calculations to project the implications of design decisions. Far from being stuck on the general inner workings of a PC, this also delves into warehousescale computing and explains other ways of constructing computers than the simple models of working but exceedingly out of date designs.

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